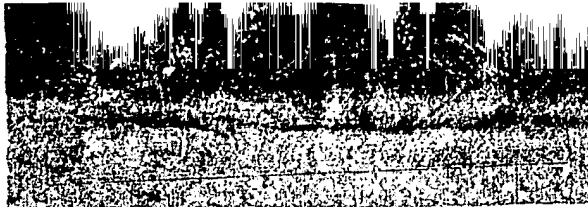


37	666
Class	Subclass
ISSUE CLASSIFICATION	



PATENT NUMBER

U.S. REISSUE Patent Application

50 SCANNED <i>ELG</i> O.A. <i>ABY</i>	O.I.P.E.	O.G. PUBLICATION DATE 1-15-02	REISSUE PATENT DATE
--	----------	----------------------------------	---------------------

APPLICATION NO. 09/987978	CONT/PRIOR D F	CLASS 257	SUBCLASS 666	ART UNIT 4734-2715	EXAMINER J. Clark
------------------------------	-------------------	--------------	-----------------	-----------------------	----------------------

APPLICANTS

Yujiro Kaihara
Kazunari Suzuki
Kunihiro Tsubosaki
Hiromichi Suzuki

BEST AVAILABLE COPY

Leadframe semiconductor integrated circuit device using the same, and method of and process for fabricating the two

TITLE

PTO-2040
12/99

CLASSIFICATION	
CROSS REFERENCE(S)	
CLASS 666	SUBCLASS (ONE SUBCLASS) 257
ART UNIT 4734-2715	ART UNIT 4734-2715
INTERNATIONAL CLASSIFICATION H01L 23/495	
<input type="checkbox"/> Continued from <i>ELG</i> File Jacket	

<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	DRAWINGS Sheets Drawn: 31 Figs. Drawn: 36 Print Figs: 3			CLAIMS ALLOWED Total Claims: 13 Print Claim for O.G.: 1	
	NOTICE OF ALLOWANCE MAILED _____ (Assistant Examiner) _____ (Date)			ISSUE FEE Amount Due: _____ Date Paid: _____	
FINAL SPRE REVIEW _____ (INITIALS) _____ (Legal Instruments Examiner) _____ (Date)			ISSUE BATCH NUMBER _____		

WARNING:

The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

Form PTO-2008
(Rev. 6/99)

FILED WITH: ☐ DISK (CRF) ☐ FICHE ☐ CD-ROM
(Attached in pocket on right inside flap)